

AMENDMENTS TO THE SPECIFICATION

Please insert the following paragraph after paragraph [1015]:

[1015.1] **FIG. 5** illustrates another exemplary OR circuit configured consistent with the present invention.

Please replace paragraph [1026] with the following replacement paragraph:

[1026] The description of the invention set forth herein is illustrative, and is not intended to limit the scope of the invention as set forth in the following claims. For example, while a specific logic function has been described, the individual functionality may vary. The individual evaluation components are not critical to understanding the invention described herein and typically varies from circuit to circuit. The OR-type circuit of FIG. 2 is for illustration only, and other circuits (such as NOR, NAND, AND, XOR, XNOR, etc.) may also benefit from the techniques described. Although the evaluation circuitry has been described as including n-channel devices and the keeper devices have been described as p-channel devices, the evaluation circuitry may include p-channel devices and the keeper devices may be n-channel devices (see, e.g., FIG. 5). Moreover, the applications in which the invention may be applied are not limited to dynamic logic circuits, but may extend to other circuits with functions similar to the requirements of dynamic ~~circuit~~ circuits. Other variations and modifications of the embodiments disclosed herein, may be made based on the description set forth herein, without departing from the scope and spirit of the invention as set forth in the following claims.